AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111
Serial Number: 10/607,772
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Title: CACHE INTEGRITY APPARATUS, SYSTEMS, AND METHODS
Assignee: Intel Corporation

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IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 1, line 16, is amended as follows:

While it [[is]] can be advantageous to use non-volatile memory in a cache (e.g., the cache state can be maintained during system shutdown or a power failure), implementation problems may arise. For example, write requests associated with data stored in a non-volatile cache that occur prior to loading the cache driver may result in data corruption once the driver boots. Invalidating the non-volatile cache after the driver begins execution may avoid data corruption, but the advantage of a persistent cached data state is lost.